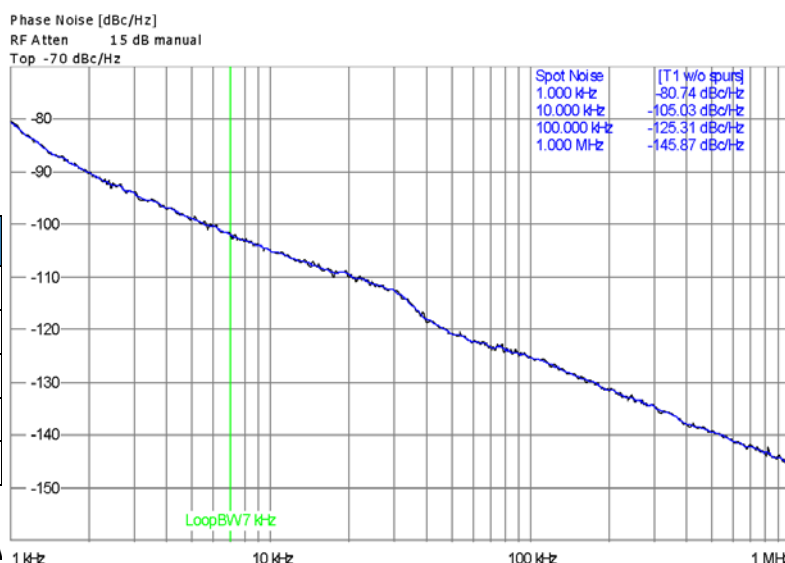
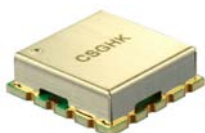


SURFACE MOUNT MODEL

VOLTAGE CONTROLLED OSCILLATOR

Frequency 1325-1706MHz

VL1706M



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning

Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5.0\text{V}$

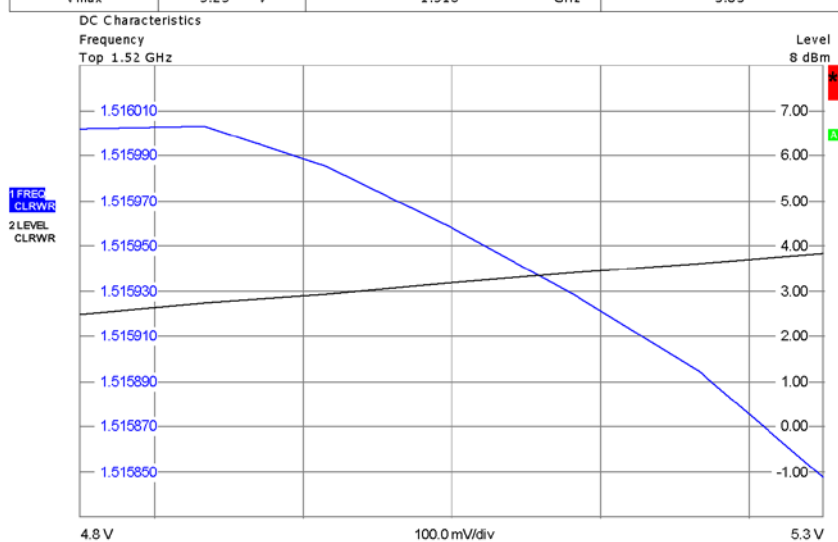
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		1325		1706	MHz
Tuning Voltage (V)		1.0		10.5	V
RF Output Power	1325-1706MHz	2.0		6.0	dBm
Supply Voltage (Vcc)		4.75	5	5.25	V
Supply Current (Icc)			20	23	mA
Phase Noise :					
	@10kHz Offset:		-104	-101	dBc/Hz
	@100kHz Offset:		-125	-122	dBc/Hz
Average Tuning Sensitivity	1325-1706MHz		55.4		MHz/V
Harmonic Outputs			-10		dBc
OperatingTemperature Range		-55		+85	$^\circ\text{C}$
Tune Input Capacitance			20		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-55℃	+25℃	+85℃	-55℃	+25℃	+85℃	-55℃	+25℃	+85℃
1.0	54.9	1258.0	1247.0	1238.4	5.2	5.4	3.3	-7.2	-10.7	-20.2
2.0	54.1	1313.6	1304.0	1296.4	4.9	5.0	3.3	-7.8	-11.7	-21.9
3.0	54.7	1367.4	1356.8	1350.4	4.8	5.1	3.5	-7.1	-11.1	-20.7
4.0	55.7	1421.6	1412.4	1404.4	4.7	4.9	3.2	-6.8	-10.8	-19.7
5.0	54.9	1477.6	1468.0	1460.4	4.2	4.4	2.9	-7.6	-11.8	-20.8
6.0	53.7	1531.8	1522.4	1514.4	4.0	4.2	3.0	-10.2	-14.1	-22.3
7.0	53.5	1587.0	1577.0	1570.4	3.5	3.9	2.8	-12.0	-16.1	-224.3
8.0	54.0	1640.4	1630.0	1622.4	3.2	3.7	2.7	-13.2	-17.9	-25.1
9.0	53.1	1694.2	1684.2	1674.6	3.1	3.4	2.6	-14.7	-18.8	-26.3
10.0	51.3	1748.0	1737.0	1725.6	2.4	3.2	2.4	-15.0	-19.8	-27.8
10.5	50.8	1773.8	1762.8	1754.2	2.6	3.2	2.1	-16.1	-19.9	-26.7

R&S FSUP Signal Source Analyzer					
Settings			Results		
Supply	Voltage		Frequency		Level
Vmin	4.75	V	1.516	GHz	2.48 dBm
Vcurrent	5.00	V	1.516	GHz	3.17 dBm
Vmax	5.25	V	1.516	GHz	3.83 dBm



Measurement Aborted

Date: 15.FEB.2014 10:49:16

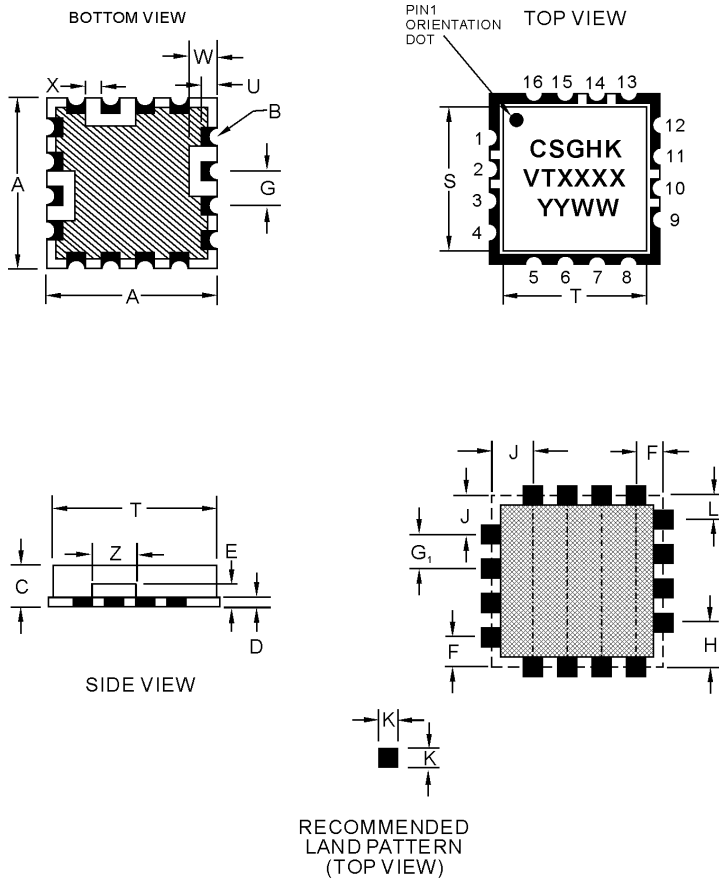
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FORM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	4.3	0.169
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			